

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)

SEC.1100

Application Number

10/788,407
NEW

Applicant(s)

Young-nam HWANG et al.

Filing Date

MARCH 1, 2004

Group Art Unit

2827
TO BE ASSIGNED

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Q	A	6,545,907	04/08/2003	LOWREY et al.			
	B	2003/0002332	01/02/2003	LOWREY			
	C	6,487,113	11/26/2002	PARK et al.			
	D	6,075,719	06/13/2000	LOWREY et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Q	E	Y.N. Hwang et al., "Completely CMOS-Compatible Phase-Change Nonvolatile RAM Using NMOS Cell Transistors"
	F	Manzur Gill et al., "Ovonic Unified Memory - A High-Performance Nonvolatile Memory Technology for Stand-Alone Memory and Embedded Applications," ISSCC 2002/Session 12/TD: Digital Directions/12.4, pages 158-159, and 445-446.

EXAMINER

Se Arbuong

DATE CONSIDERED

11/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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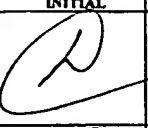
Applicant(s)
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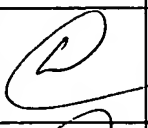
Filing Date
March 1, 2004

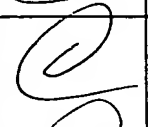
Group Art Unit **2837**
TO BE ASSIGNED


*EXAMINER
INITIAL

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

 Stefan Lai et al., "OUM - A 180 nm Nonvolatile Memory Cell Element Technology For Stand Alone and Embedded Applications," IEDM 01-803 - 806.

 Y.N. Hwang et al., "Phase-Change Chalcogenide Nonvolatile RAM Completely Based on CMOS Technology"

 Linda Geppert, "The New Indelible Memories," IEEE Spectrum, March 2003, pages 49-54.

 Jon Maimon et al., "Chalcogenide-Based Non-Volatile Memory Technology," pages 5-2289 - 5-2294.

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